

**METHOD AND APPARATUS FOR TRANSMITTING/RECEIVING  
HIGH ORDER DIGITAL SIGNALS OVER TWO RF CARRIERS  
IN A RADIO REGENERATION SECTION**

**BACKGROUND OF THE INVENTION**

**1. Field Of The Invention**

The present invention relates to radio transmissions and in particular it concerns a method and apparatus for transmitting/receiving STM-4 (SDH) or STS-12 (SONET) digital signals over two RF carriers in a respective SDH or SONET radio regenerator section.

**2. Description Of The Prior Art**

In present telecommunication systems, the need often arises (particularly with operators who have to manage congested networks) of providing high capacity radio systems with high spectrum efficiency and not much complex architecture. These radio systems are required to transmit high order digital signals, such as STM-4 (SDH) or STS-12 (SONET) synchronous hierarchy levels, at 622.08 Mb/s. Such links must be capable of being inserted in optical networks on STM-4 interface nodes with all the features of performance monitoring, management, protection, etc... The above needs are also apparent from the last issues of some ETSI Recommendations.

The solution which is at present known for interconnecting an STM-4 data stream with a radio equipment provides for the transmission of four STM-1 signals over four corresponding RF carriers. In essence, it is a solution providing for the transmission of four STM-1 signals in the radio section and allows the transport of an STM-4 stream in a regenerator section, by using four RF carriers in a 4+2 protection system configuration.

It is clear that this type of approach does not optimize the spectral efficiency of the transmission system. Moreover, it requires the use, in the radio link, of four transceivers (in addition to two spare transceivers) and the management of four STM-1 channels in a minimal configuration of the protection system that contemplates the use of two protection channels (4+2 protection).

**SUMMARY OF THE INVENTION**

In view of the above needs and of the known solutions which are not considered as completely efficient, it is the main object of the present invention to provide a method and an apparatus for transmitting/receiving high order digital signals over two RF carriers in a radio regenerator section, such as STM-4 signals in an SDH radio regeneration section or STS-12 signals in a SONET radio regeneration section.

The main object of the present invention is providing a method of radio transmitting high order synchronous digital signals wherein it comprises the steps of terminating the Regenerator Section OverHead bytes of the received standard frames and performing a computation of the parity byte; providing a Radio Frame Complementary OverHead; transmitting the terminated Regenerator Section OverHead bytes and the Radio Frame Complementary OverHead interleaved by columns in a 1+1 protected configuration; and wherein said step of subdividing the received standard frame into a number of sub-frames comprises the step of subdividing by columns the standard frame into two sub-frames for transmitting them over working channels by using only two RF carriers without performing any multiplexing/demultiplexing operation.

A further object of the present invention is providing a method of receiving high order synchronous digital signals which have been transmitted via radio, wherein it comprises a step of combining sub-frames, said step of combining sub-frames comprising the step of recomposing by columns the received sub-frames through a mapping process based on the recognition of the respective alignment words and of the header to identify the correct sequence of the two received sub-frames; and wherein it comprises the further steps of extracting the Regenerator Section OverHead bytes from a Radio Frame Complementary OverHead and inserting said Regenerator Section OverHead bytes in the respective positions after the step of recomposing by columns.

A still further object of the present invention is providing an apparatus for radio transmitting high order digital signals, wherein said apparatus comprises means for terminating the header bytes of the received frames and performing computation of a parity byte; means for sending the terminated RSOH bytes to a frame complementary section for the transmission thereof in a 1+1 protection scheme, interleaved by columns with the sub-frames, over two working channels or over a working channel and a protection channel; and means for subdividing the received standard frame into a number of sub-frames, said subdividing means comprise means designed to subdivide by columns the standard frame into two sub-frames for transmitting them over working channels by using only two RF carriers without performing any multiplexing/demultiplexing operation.



- Fig. 4 shows the AUOH pointers of an STM-4 signal before and after the subdivision of the signal itself into two sub-frames;
- Fig. 5 shows the header of an STM-4 frame;
- Fig. 6 shows the four bytes of the alignment word that are generally used by a state machine to detect the alignment condition;
- Fig. 7 shows the alignment strategy used for an STM-4 frame;
- Fig. 8 shows, in two STM-4/2 sub-frames, the bytes that are used as headers to identify their arrival order at the receiver side;
- Fig. 9 shows the alignment device of the STM-4/2 sub-frames; and
- Fig. 10 shows the finite state diagram of the alignment strategy used for the STM-4 frame and for the STM-4/2 sub-frames.

### **BEST MODE FOR CARRYING OUT THE INVENTION**

As said above, in order to transmit high order digital signals from one radio equipment to another radio equipment in a transmission system, the present invention substantially provides for subdividing, in a manner that will be described later on, the frames of the signals to be transmitted into two sub-frames. These two sub-frames are then transmitted by using only two RF carriers, without performing any signal multiplexing or demultiplexing operation, and without any pointer processing, thus using the radio system as a pure regenerator network element (NE).

The method of the invention, hereafter described for the transmission of STM-4 SDH signals, consists in dividing the STM-4 signal which is present at the network interface (namely, at the input of the transceiver) into two STM-4/2 signals to be transmitted over two modulated carriers in proper channel spacing and with a minimal configuration (2+1) of the protection system. Clearly, "channel spacing" means the distance between two adjacent frequencies in the channeling used.

For example, in a bandwidth of about 55 MHz and by using two cross-polar carriers at the same frequency with a Cross-Polar Interference Canceller (XPIC) it is possible to transmit an STM-4 signal, thus increasing the spectrum efficiency of the system up to 622.08 Mbps/55MHz= 11.31 bit/s/Hz. It is to be noticed that the transmission of the two STM-4/2 signals is independent of the radio frequency used and of the modulation scheme applied. Accordingly, it is possible to implement the considerations that will be described below both in the short-haul systems and in the long-haul ones.

With a high integration level of the base band signal processing, of the hitless protection system and of the service auxiliary channel transmission, the system architecture according to the invention can meet all the requirements necessary for being used in STM-4 (STS-12) transport networks, STM-4 (STS-12) access areas and STM-4 (STS-12) ring networks.

As it is known, at present the transmission of an STM-4 signal over limited-band radio channels occurs through a multiplex system. In this event the Multiplex Section (MST) between two Network Nodes should be interrupted by additional multiplex/demultiplex sections. This fact is against the SDH network philosophy, and in disagreement with the functional requirements stated by the Recommendations.

For this reason, the STM-4 radio-relay system according to the invention comprises Regenerator Sections (RST) only, without any multiplex/demultiplex operation being carried out.

This solution guarantees that all information contained in the Virtual Containers, the Multiplex Section Overhead (MSOH) and the AUOH pointer row are transmitted in a rather transparent manner inside the radio section, where only the RSOH bytes are processed.

In order to understand the de-interleaving and interleaving mechanisms described hereinafter (indeed exploiting the symmetry characteristics of the structures contained in the SDH frames), consider the allocation of AU-4 (VC-4) structures in an STM-4 frame.

The STM-4 frame may comprise four AUG-1s, that will be numbered from #1 to #4:

- AUG-1#1 is allocated in columns 1, 5, 9, 13,... of the STM-4 frame;
- AUG-1#2 is allocated in columns 2, 6, 10, 14,... of the STM-4 frame;
- AUG-1#3 is allocated in columns 3, 7, 11, 15,... of the STM-4 frame; and
- AUG-1#4 is allocated in columns 4, 8, 12, 16,... of the STM-4 frame.

Each AUG-1 may comprise three AU-3 structures, that will be numbered from #1 to #3. Therefore, each AU-4 can be identified by a number in the form #B, #A, where B indicates the AUG-1 number (from 1 to 4), and A is always 0.

The identification of the columns, in the STM-4 frame, occupied by the AU-4 (B,0) structures is given by:

$$X\text{th column} = 1 + [B-1] + 4*[X - 1] \quad (\text{for } X = 1 \text{ to } 270).$$

As a consequence, the AU-4 (1,0) structure resides in the columns 1, 5, 9,..., 1077 of the STM-4 frame, and the AU-4 (4,0) structure resides in the columns 4, 8, 12,..., 1080 of the STM-4 frame.



As SOH bytes are not used for the transmission of the service channels in the radio section, the parity controls in the B1 and B2 bytes are in no way altered. The performance monitoring parameters on the STM-4 signal may then be calculated out of the the exchange section starting from the computation of the parities on B1.

Before concluding the de-interleaving process analysis, it is useful to stop on the behaviour of the pointers (Fig. 4) of the four VC-4s which are present on the AUOH (Administrative Unit OverHead) row of the STM-4 signal. As it is known, the bytes of the four pointers are arranged adjacently on the AUOH row and following upon the de-interleaving process, place themselves in the homologous rows of the two sub-frames, but they loose their original configuration.

Consider in Fig. 4 the AUOH configuration in the case where the payload sub-matrix must accomodate AU-4 structures. The two transmitted sub-frames will thus have an invalid pointer structure and only in the STM-4 frame reconstruction phase (at receiving side) it will be recomposed correctly.

Considerations similar to those made for the transmitter apparatus can be made for the apparatus receiving the STM-4/2 sub-frames and combining them again to obtain the same frame received by the transmitter apparatus. The receiver apparatus is schematically illustrated in Fig. 2.

In the receiver apparatus (RX), the RFCOH bytes are extracted from the received signals and the two STM-4/2 sub-frames are interleaved by columns through a mapping process based on the recognition of the respective alignment words and of the header which identifies the correct sequence of the two sub-matrices received for the standard STM-4 frame reconstruction. In fact, when the de-interleaving process in transmission divides the frames (by columns) into two sub-frames, inserted in the alignment word are two different headers that color its order of decomposition. In this way, during the reconstruction of the STM-4 frame in reception, the alignment device is able to identify the correct sequence of the two received sub-frames, and the interleaving does not alter the order thereof.

In the reconstruction step, the two sub-frames will place themselves correctly in the frame and also the AUOH pointer will automatically indicate the position of the first byte of the first VC-4 contained in the primitive STM-4 structure.

The hitless exchange structure operates on the STM-4/2 signals transmitted over the radio channel, managing a high priority level for that sub-frame containing the MSOH bytes (DCCM, E2, S1, M1, etc...) considered of main importance and that in the above-

described de-interleaving process applied to the STM-4 frame, automatically place themselves in the columns  $X[i]$  (where  $i=1, 7, 13$ ) of the first sub-frame.

The overhead of an STM-4 frame is represented in Fig. 5 where "x" denotes the bytes reserved for national use, "\*" denotes the unscrambled bytes that must be handled with special care, and "Δ" denotes the bytes dependent on the transmission medium. In an STM-4 frame the alignment word is composed of 24 bytes (12xA1 and 12xA2), but the state machine that detects the alignment condition, as it is known, works only on the four central bytes, namely A1, A1, A2, A2 (see Fig. 6).

The alignment strategy used for an STM-4 frame considers a "short word" (from the alignment state to the OOF (Out Of Frame) condition) and a "long word" (from the OOF condition to the alignment state), as shown by way of example in the table of Fig. 7.

In an implementation of the device according to the present invention, the parameters that characterize the performance of the STM-4 aligning device, whose finite state diagram is illustrated in Fig. 10, are summarized in the following table:

parameters	
Average recovery T (ms)	0.259213
Max recovery T (ms)	0.393239
Forced loss T (s)	522581
Min. forced loss T (s)	508
Alignment loss average frame N	5
Alignment loss average frame N variance	0.116
Probability of false alignment loss	$2.7 \times 10^{-10}$

When the de-interleaving process on the transmission side divides the frames by columns into two different sub-frames, the same alignment word is generated for each of the two sub-frames. Hence the algorithms used in the alignment devices for the STM-4 and STM-4/2 frames are the same since they operate on the bytes A1 A1 A2 A2 of the sole central portion of the entire alignment word (bytes # 1.11, #1.12, #1.13 and #1.14 for the STM-4 standard frame and bytes #1.5, #1.6, #1.7 and #1.8 for the STM-4/2 frames). Moreover, it is necessary to univocally identify the correct sequence of the two subsets extracted from the STM-4 frames. To this end, the four A2 bytes (#1.9, #1.10, #1.11, #1.12) of the alignment word directly adjacent to the area in which the alignment device operates that will be used as a sort of "header" for the two sub-frames, are "colored". For instance (see also Fig. 8):

A2 A2 A2 A2 = 00001111 00001111 00001111 00001111 = 0F 0F 0F 0F



for the sub-frame N.1

A2 A2 A2 A2 = 01010101 01010101 01010101 01010101 = 55 55 55 55

for the sub-frame N.2

Thus, during the process of reconstructing the original STM-4 signal at the reception side (RX), the interleaving algorithm is able to identify the correct sequence of the two sub-frames transmitted in the radio channel by reading the configuration of the header bytes A2 after the alignment. Finally, the complete standard alignment word (24 bytes) is rewritten in the reconstructed STM-4 frame.

Using the solution described above, there is no need to synchronize the signals in the functional blocks described. In fact, at the transmission side (TX), the clock used for the de-interleaving mechanism and for generating the two STM-4/2 sub-frames is derived directly from the STM-4 standard signal coming into the system. At the reception side (RX), the clock used in the reconstruction process (interleaving) of the STM-4 frame can be directly selected from at least one of the STM-4/2 signals transmitted in the radio section synchronous with the incoming STM-4 signal. The failure indications on the clock extractor (LOS CK) will indicate the need to use the clock of the other STM-4/2 signal.

In the section for the reconstruction of the STM-4 frame (at reception side), it is necessary to carry out an alignment procedure of the two STM-4/2 streams transmitted in the radio section, as for instance shown in Fig. 9. It is necessary to compensate the possible clock shifts due to propagation phenomena induced on the radio transmission channel and to differences of electrical paths (length of the feeders, etc...). The alignment system provides for the use of a pair of elastic memories and a 311.04 MHz PLL connected to the clock selection block.

It can be assumed that propagation phenomena (fading) may cause a clock shift equal to about one half symbol period corresponding to  $\pm 4$  bits. In fact, by using a 128-state modulation scheme, the length of the transmitted symbol is equal to 7 bits, as results from the following considerations:

□ Modulation scheme	128 QAM
□ Bit rate (without redundancy)	311.04 Mb/s
□ Bit period	$T_b = 3.2 \text{ ns}$
□ Number of bits per constellation symbol	7 bits
□ Symbol period	$T_s = T_b \times 7 \text{ bits} = 22.4 \text{ ns}$
□ Delay due to fading	$\pm T_s/2 = \pm 11.2 \text{ ns}$
□ Delay due to fading (in bits)	$\pm 11.2 \text{ ns}/T_b = \pm 3.5 \text{ bits}$

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This means that at the input of the two elastic memories before the frame interleaving section (radio reception side) the situation could be such that every sub-frame is  $\pm 4$  bit staggered relative to one another.

The read clock of the two elastic memories is always derived from only one clock, which should be the first or one of the others in the case where the first clock is degraded.

Finally, it is convenient to oversize the elastic storages so as to take into account other effects (jitter, different lengths of the electrical paths, etc...) that may cause additional delays.

In case it is not possible performing the alignment between the two signals (for instance because of too large unrecoverable delays) or when one of the two STM-4/2 signals is lost at the output of the exchange section, it is necessary to send a signalling to the output block RST (line side) in order that a MSAIS alarm is inserted in the STM-4 frame and this is completely regenerated, re-calculating its parity on byte B1 and inserting the DCCR, E1, F1 channels, and any other possible service channels (anyhow protected in 1+1 configuration in the radio section).

Although the configuration which is considered the best one provides for two working channels and one protection channel, the present invention could be realized without the protection channel or possibly with more than one protection channels, as well.

There have thus been shown and described novel methods and novel apparatus for radio transmitting/receiving high order digital signals which fulfill all the objects and advantages sought therefor. Many changes, modifications, variations and other uses and applications of the subject invention will, however, become apparent to those skilled in the art after considering the specification and the accompanying drawings which disclose preferred embodiments thereof. All such changes, modifications, variations and other uses and applications which do not depart from the spirit and scope of the invention are deemed to be covered by the invention which is limited only by the claims which follow.